

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Steven J. Koester

Examiner: Anh D. Mai

Serial No: 10/717,279

Art Unit: 2814

Filed: November 19, 2003

Docket: YOR920030533US1 (17110)

For: SEMICONDUCTOR FIELD-EFFECT
TRANSISTOR HAVING STRAINED-LAYER
(Previously Amended)

Dated: June 13, 2007

Confirmation No: 7401

Mail Stop AF
Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action dated March 13, 2007, constituting a Final Rejection, applicants respectfully request the Examiner to reconsider the application in view of the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on June 13, 2007.

Dated: June 13, 2007


Steven Fischman